PDFN5×6 N

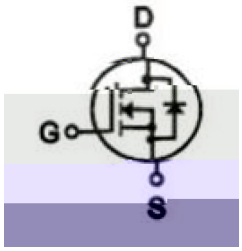
N-Channel MOSFET in a PDFN5×6 Plastic Package.

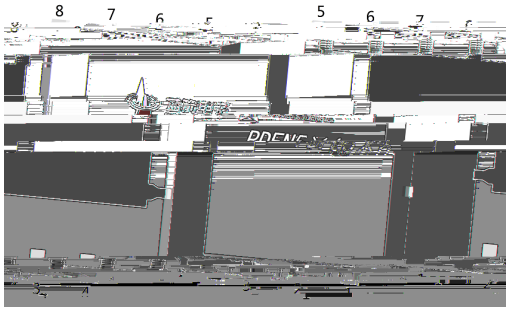
V_{DS} (V) = 100V I_D =62.5A (V_{GS} = ±20V)

$R_{DS(ON)}$@10V≤10mR(Typ.8.9mR)

HF Product.

High Frequency Switching and Synchronous Rectification.

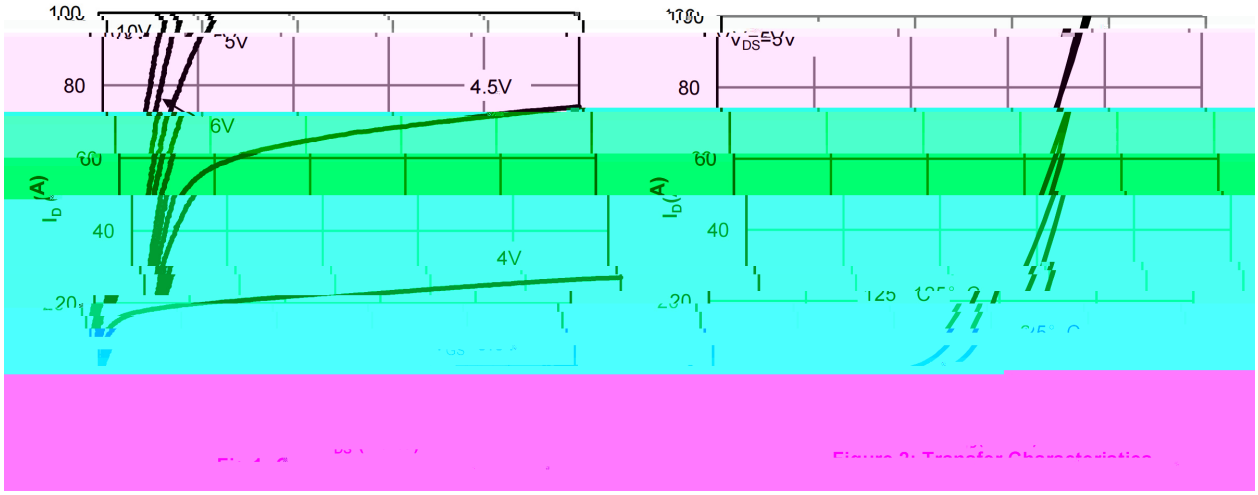


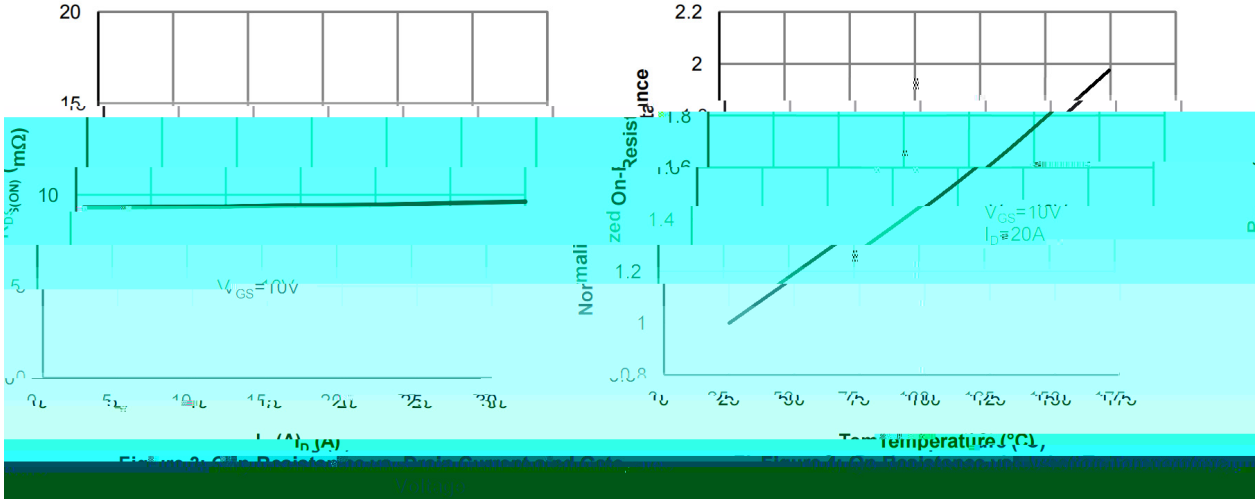


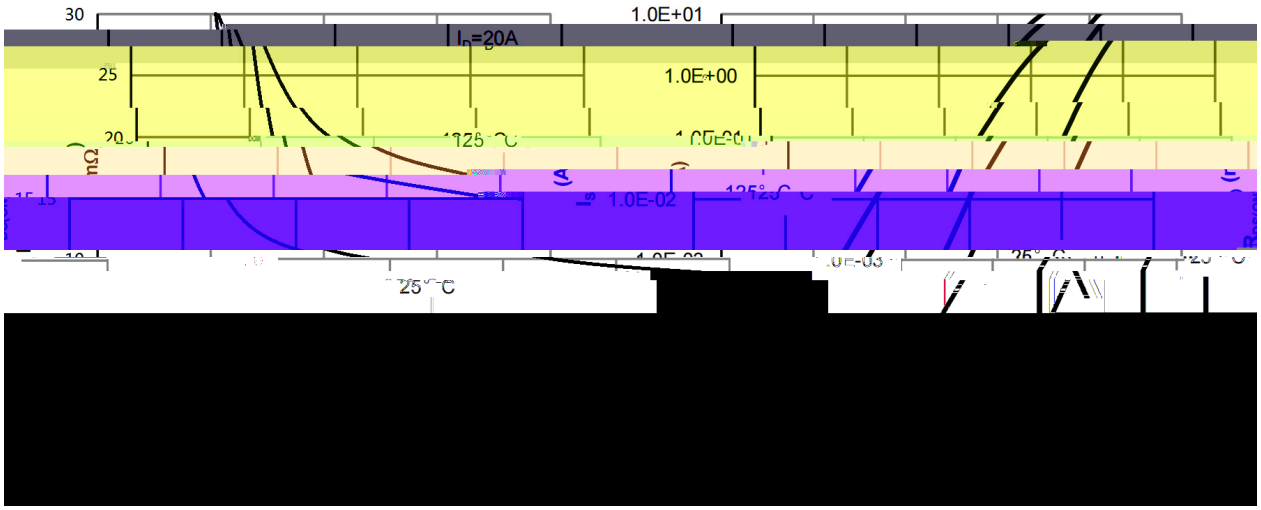
PIN1 2 3 S PIN4 G PIN5 6 7 8 D

See Marking Instructions.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Turn-On Delay Time	$t_{D(on)}$	V_{GS}=10V, V_{DS}=50V, R_L=2.5Ω, R_{GEN}=3Ω		8.5		ns
Turn-On Rise Time	t_r			3		
Turn-Off Delay Time	$t_{D(off)}$			23		
Turn-Off Fall Time	t_f			3.5		


80
4.5V
6V
4V
40
I_D (A)


20
15
10
V_{GS}=10V
2.2
2
1.4
1.2
1
V_{GS}=10V
I_D=20A


30
25
I_D=20A
1.0E+01
1.0E+00
1.0E-02
125°C
25°C

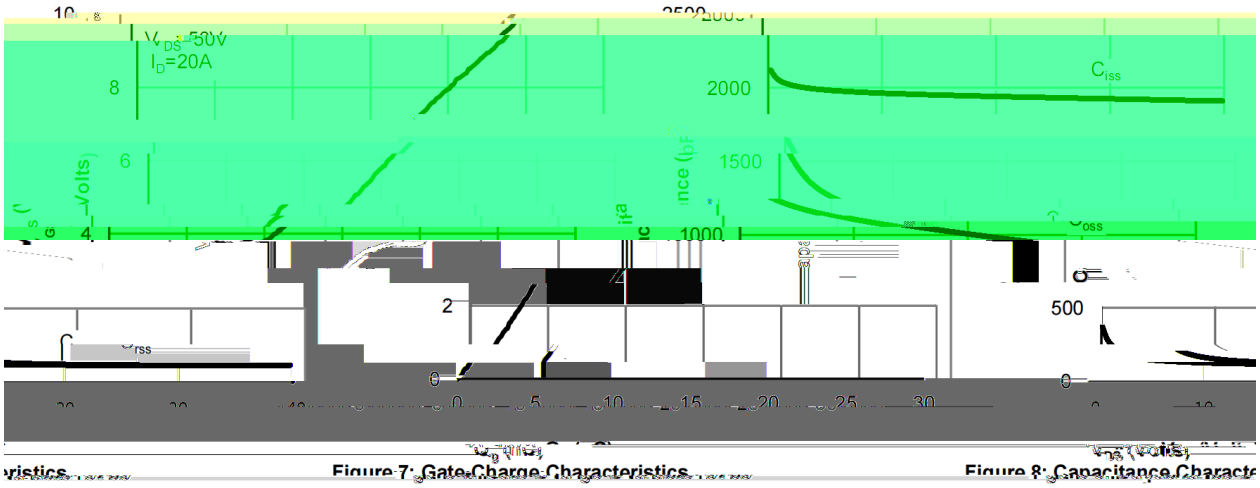


Figure 7: Gate-Charge Characteristics

Figure 8: Capacitance Characte

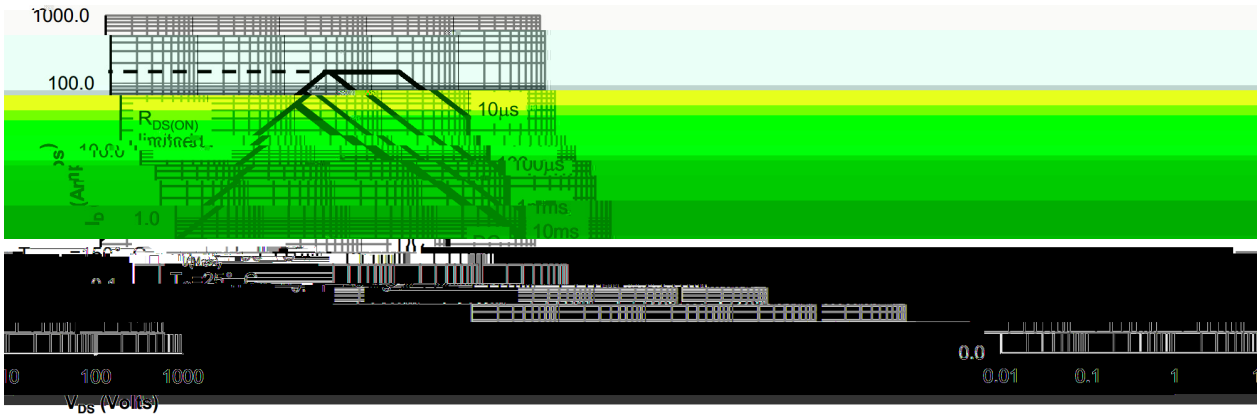
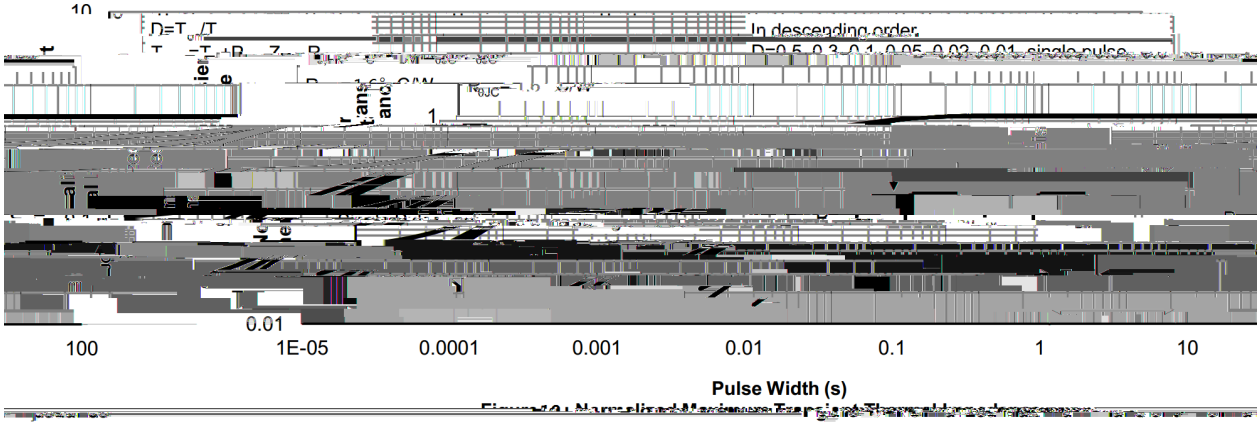


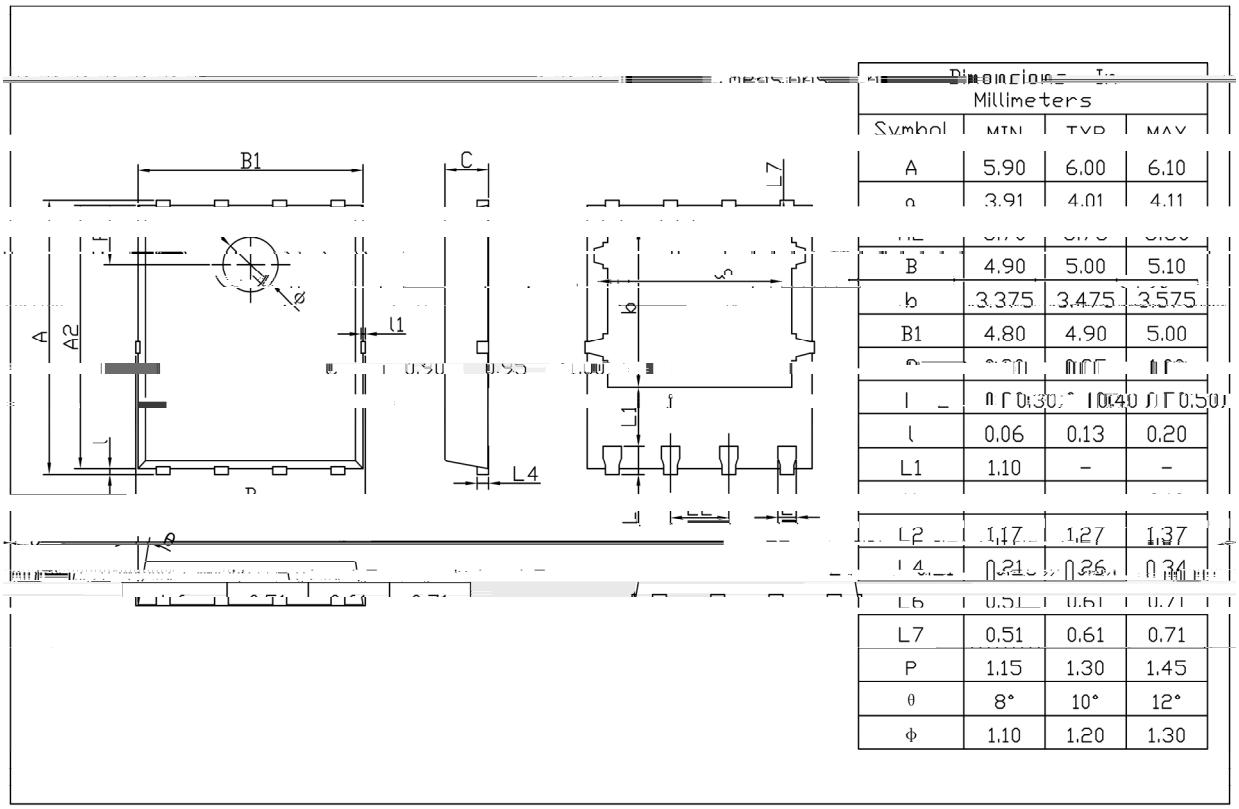
Figure 9: Maximum Forward Biased

Figure



PDFN5×6

Unit:mm



Dimensions In Millimeters			
Symbol	MIN	TYP	MAX
A	5.90	6.00	6.10
[illegible]	3.91	4.01	4.11
B	4.90	5.00	5.10
b	3.375	3.475	3.575
B1	4.80	4.90	5.00
[illegible]	[illegible]	[illegible]	[illegible]
l	[illegible]	[illegible]	[illegible]
l	0.06	0.13	0.20
L1	1.10	–	–
[illegible]	[illegible]	[illegible]	[illegible]
L2	1.17	1.27	1.37
L4	[illegible]	[illegible]	[illegible]
L6	[illegible]	[illegible]	[illegible]
L7	0.51	0.61	0.71
P	1.15	1.30	1.45
θ	8°	10°	12°
Φ	1.10	1.20	1.30

Rev.01 202209



BR

100N10SH

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Note

BR	Company Code
100N10SH	Product Type Code
****:	Lot No. Code, code change with Lot No