/ Descriptions

TO-92(R) PNP Silicon PNP transistor in a TO-92(R) Plastic Package.

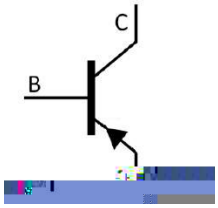
/ Features

High Voltage, Low Noise.

/ Applications

Low noise amplifier application.

/ Equivalent Circuit



/ Pinning



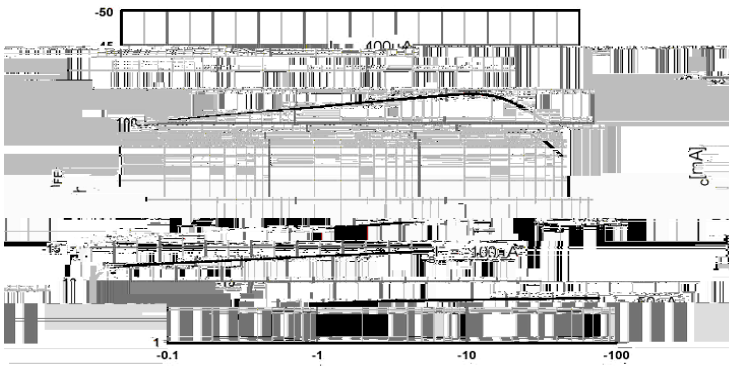
PIN1 Emitter PIN 2 Base PIN 3 Collector

/ h_{FE} Classifications & Marking

h_{FE} Classifications Symbol	A	B
h_{FE} Range	110 220	200 450

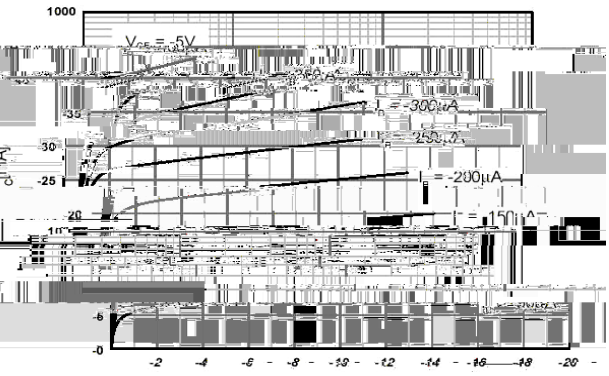
/ Electrical Characteristic Curve

$I_C - V_{CE}$



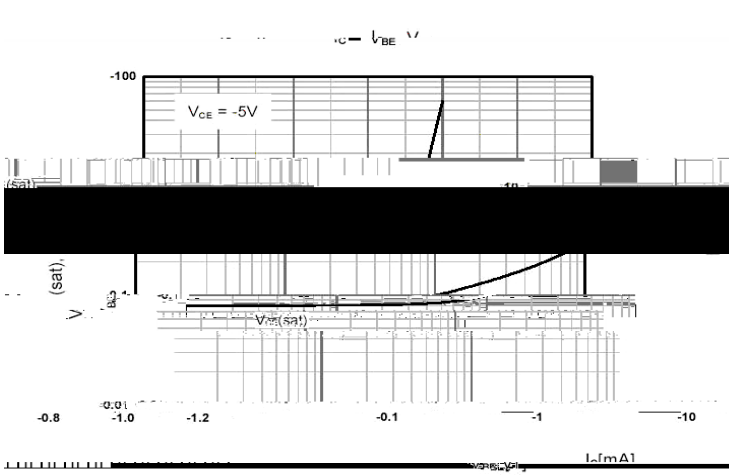
I_C[mA]

$h_{FE} - I_C$



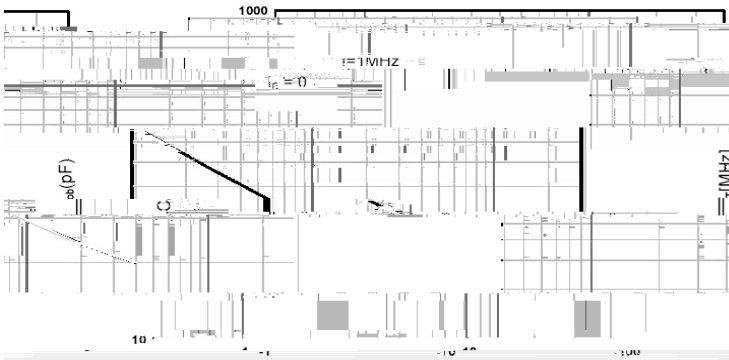
V_{CE}[V]

V_{BE}(sat) – I_C



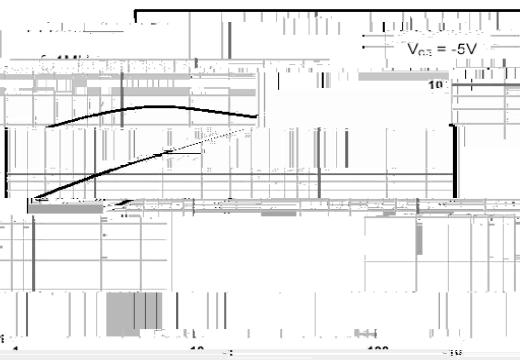
I_C[mA]

$f_T - I_C$



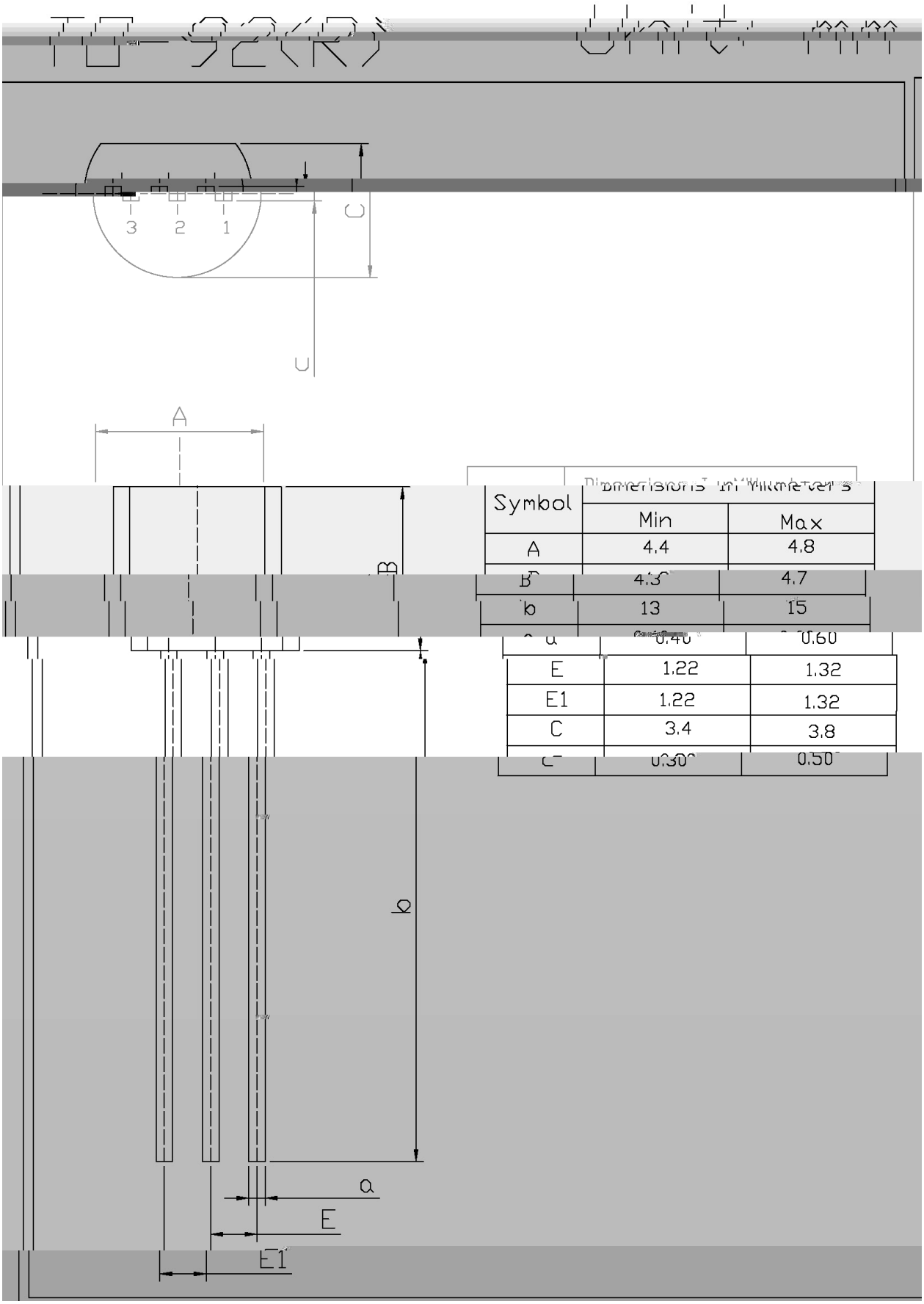
V_{CB}[V]

$C_{ob} - V_{CB}$



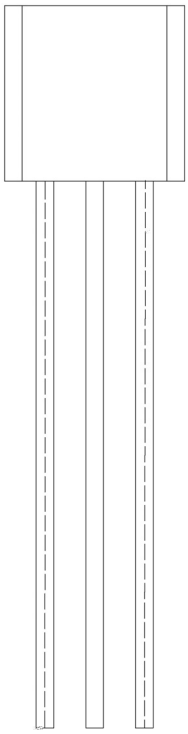
I_C[mA]

/ Package Dimensions



Symbol	Dimensions in millimeters	
	Min	Max
A	4.4	4.8
B	4.3	4.7
b	13	15
a	0.40	0.60
E	1.22	1.32
E1	1.22	1.32
C	3.4	3.8
c	0.30	0.50

/ Marking



( ) / Temperature Profile for Dip Soldering(Pb-Free)



					Note:
1	25	150	60	90sec;	1.Preheating:25~150 , Time:60~90sec.
2	255±5			5±0.5sec;	2.Peak Temp.:255±5 , Duration:5±0.5sec.
3		2	10	/sec.	3. Cooling Speed: 2~10 /sec.

/ Resistance to Soldering Heat Test Conditions

270±5	10±1 sec.	Temp.:270±5	Time:10±1 sec

/ Packaging SPEC.

/ BULK

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm3)		
	Units/Bag 只/袋	Bags/Inner Box 袋/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Bag 袋	Inner Box 盒	Outer Box 箱
TO-92	1,000	10	10,000	5	50,000	135×190	237×172×102	560×245×195
	1,000	10	10,000	10	100,000	135×190	237×172×102	560×245×375

/ AMMO

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm3)	
	Units/tape 只/纸带	Tape/Inner Box 纸带/盒	Rows/Inner Box 纸带层/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Inner Box 盒	Outer Box 箱
TO-92	3,000	1	120	10	30,000	328×230×42	小箱 480×346×235， 大箱 547×407×268

/ Notices