/ Descriptions

WR0<5+U, ṂṂÞ QSQ ÆṀ ч s ‖ ㄱ Silicon NPN transistor in a TO-92(R) Plastic Package.

/ Features

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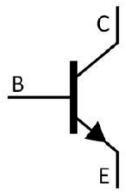
High Voltage, Low Noise.

/ Applications

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General purpose and switching application.

/ Equivalent Circuit



/ Pinning

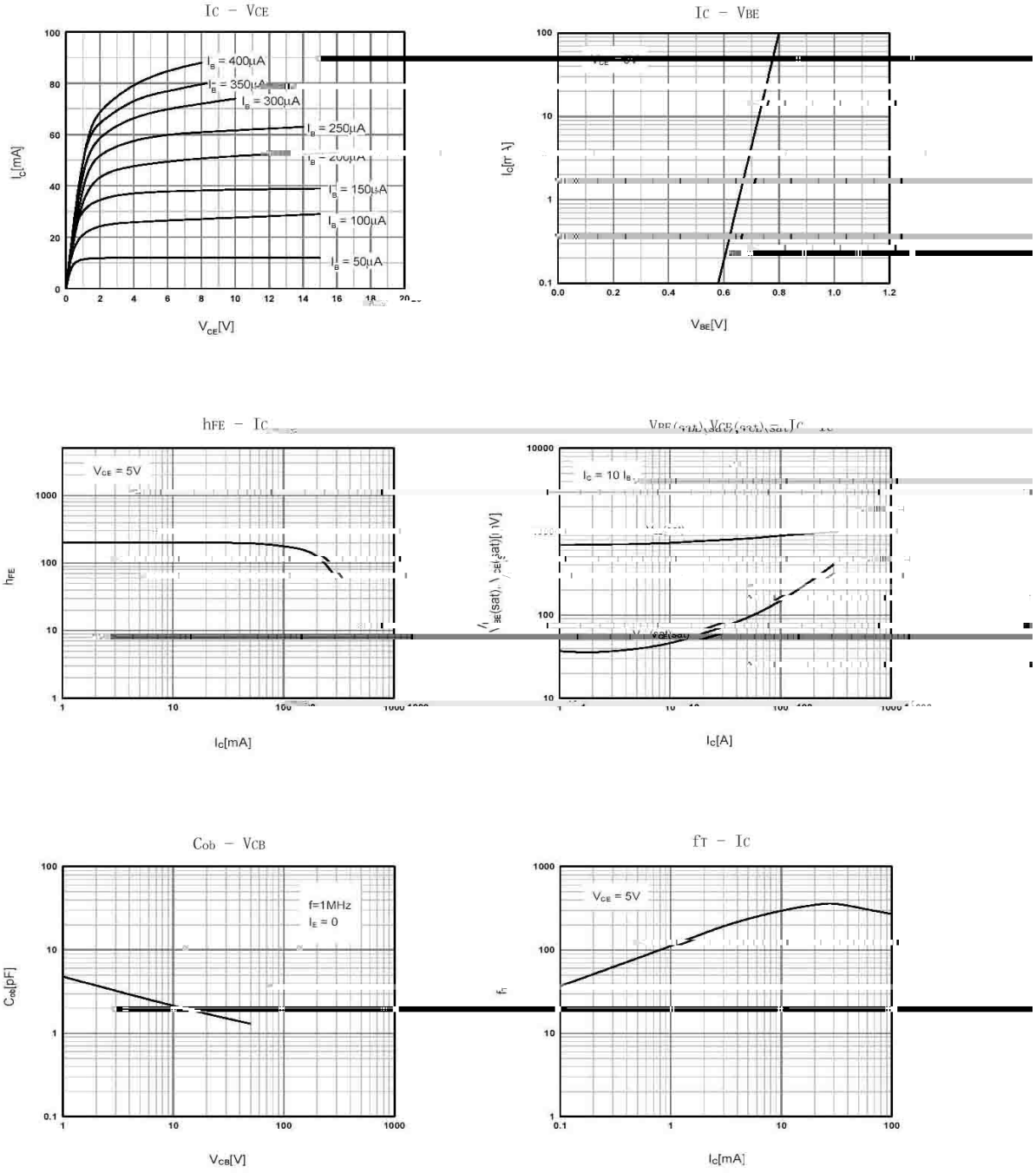


PIN1 χ Emitter PIN 2 χ Base PIN 3 χ Collector

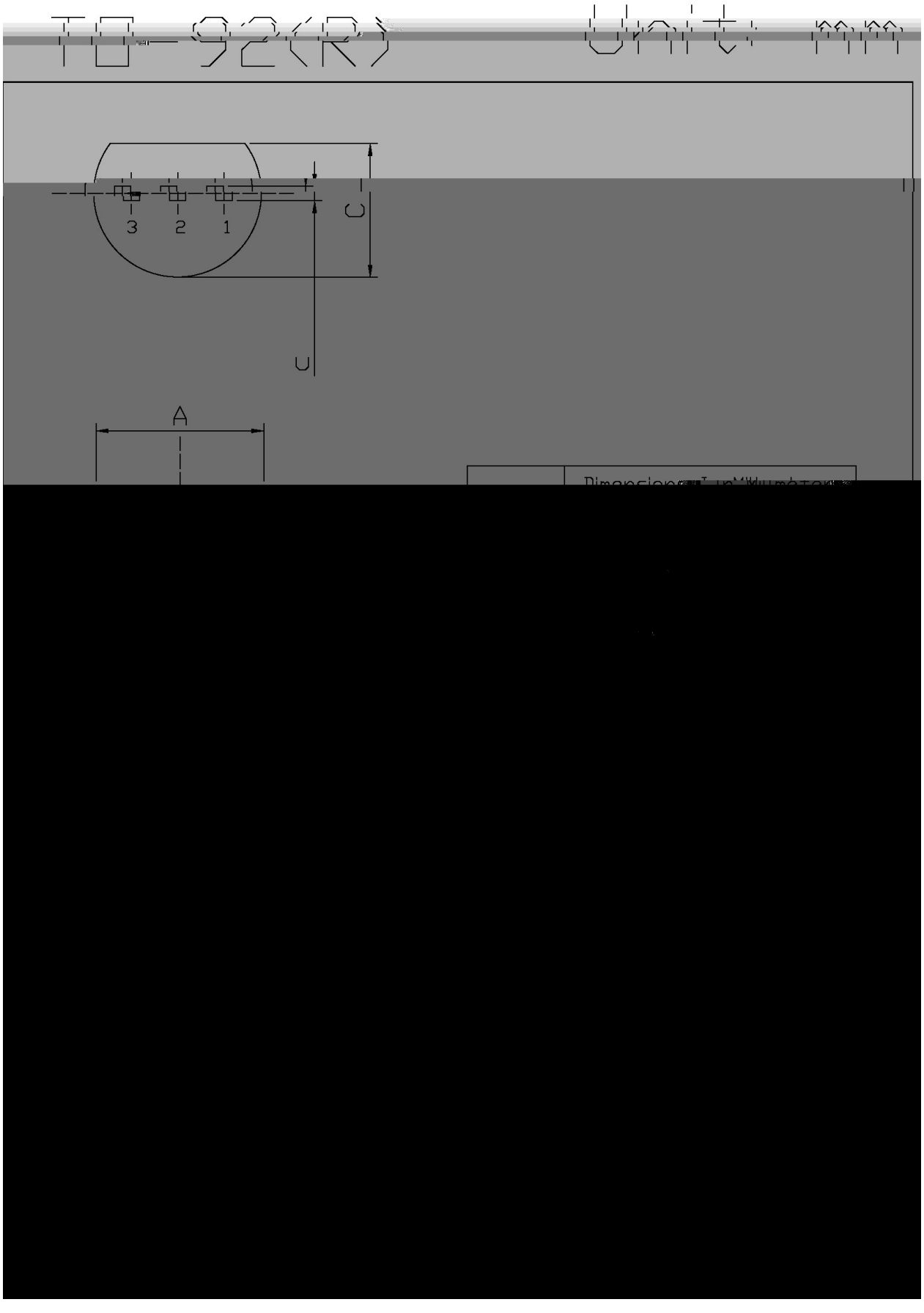
/ h_{FE} Classifications & Marking

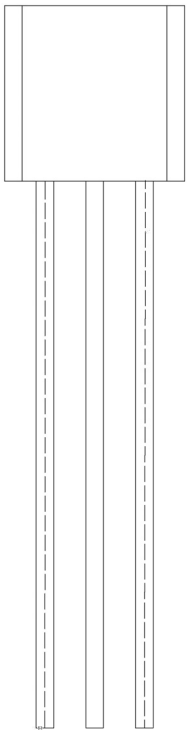
h_{FE} Classifications Symbol	A	B	C
h_{FE} Range			

/ Electrical Characteristic Curve



/ Package Dimensions





( ) / Temperature Profile for Dip Soldering(Pb-Free)



ب ˝ χ

1 Ĺ ♬ □ ỷ 25 Ḱ 150 ɬ ı `H ڑ 60 Ḱ 90sec;

2 Š Ө □ ỷ 255 ° 5 ɬ ı `H ڑ `Y ﾊ ㎛ 5 ° 0.5sec;

3 ì ˙ ｲ ˛ ɔ ﻩ ỷ ㎛ 2 Ḱ 10 ɬ /sec.

Note:

1.Preheating:25~150 ɬ , Time:60~90sec.

2.Peak Temp.:255 ° 5 ɬ , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ɬ /sec.

/ Resistance to Soldering Heat Test Conditions

□ ỷ χ 270 ° 5 ɬ `H ڑ χ 10 ° 1 sec. Temp.:270±5 Time:10±1 sec

/ Packaging SPEC.

ˈQ ţ ｙ Ь / BULK

Package Type	Units					Dimension		(unit mm3)
	Units/Bag /	Bags/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Bag	Inner Box	Outer Box
TO-92	1,000	10	10,000	5	50,000	135×190	237×172×102	560×245×195
	1,000	10	10,000	10	100,000	135×190	237×172×102	560×245×375

ﾊ Ṿ ｙ Ь / AMMO

Package Type	Units					Dimension	(unit mm3)
	Units/tape /	Tape/Inner Box /	Rows/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Inner Box	Outer Box
TO-92	3,000	1	120	10	30,000	328×230×42	480×346×235 547×407×268

/ Notices