2SC2060

Rev.E Mar.-2016

DATA SHEET

/ Descriptions

WR0<50P ṂṂÞ QSQ ÆṀч s ‖ ㄱ Silicon NPN transistor in a TO-92LM Plastic Package.

/ Features

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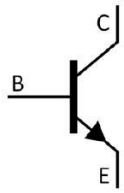
High P_C, low collector saturation voltage, complementary pair with 2SA934.

/ Applications

ƪ̂ 4Ќ5Z Ӈ ļ ѽ ƶ

1 2W low frequency amplifiers.

/ Equivalent Circuit



/ Pinning



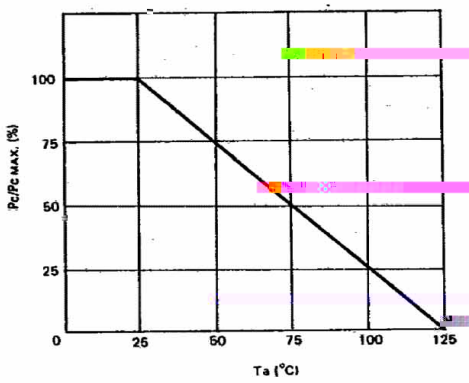
PIN1 χ Base PIN 2 χ Collector PIN 3 χ Emitter

/ h_{FE} Classifications & Marking

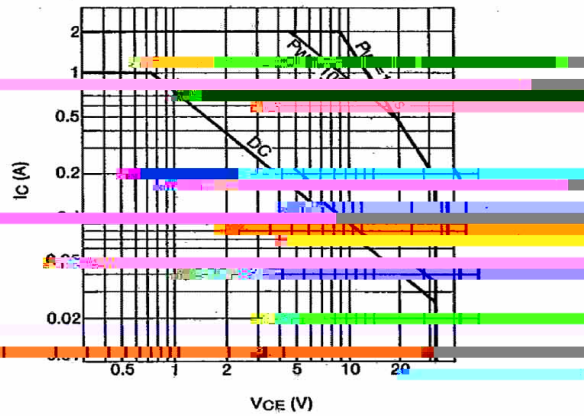
h_{FE} Classifications Symbol	P	Q	R
h_{FE} Range	82 180	120 270	180 390

/ Electrical Characteristic Curve

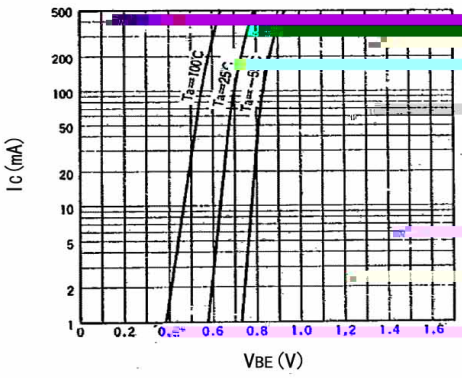
Pc – Ta



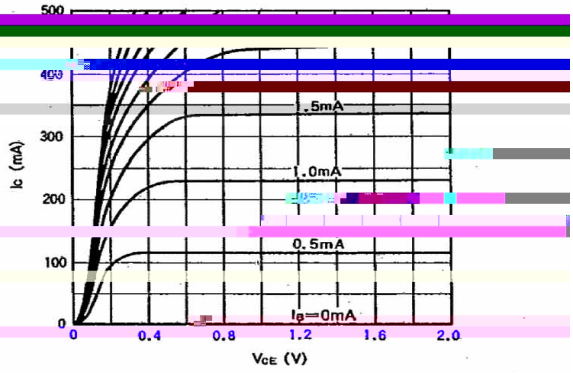
Ic – VCE



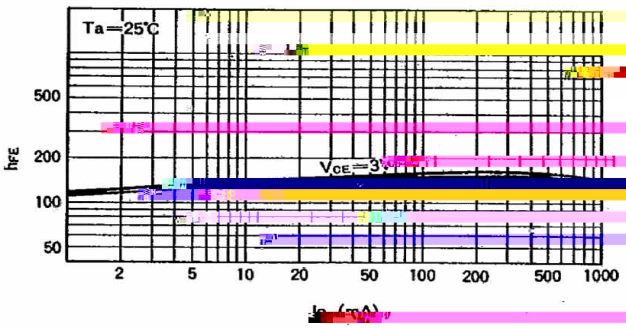
Ic – VBE



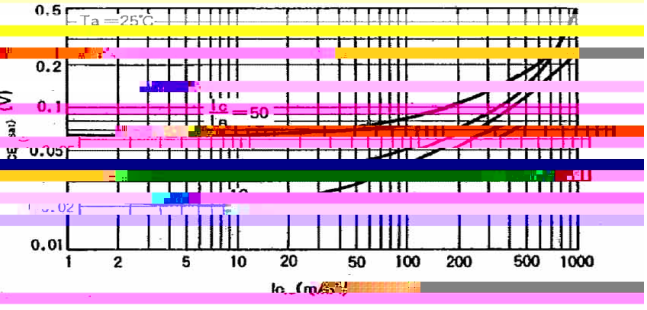
Ic – VCE



hFE – Ic

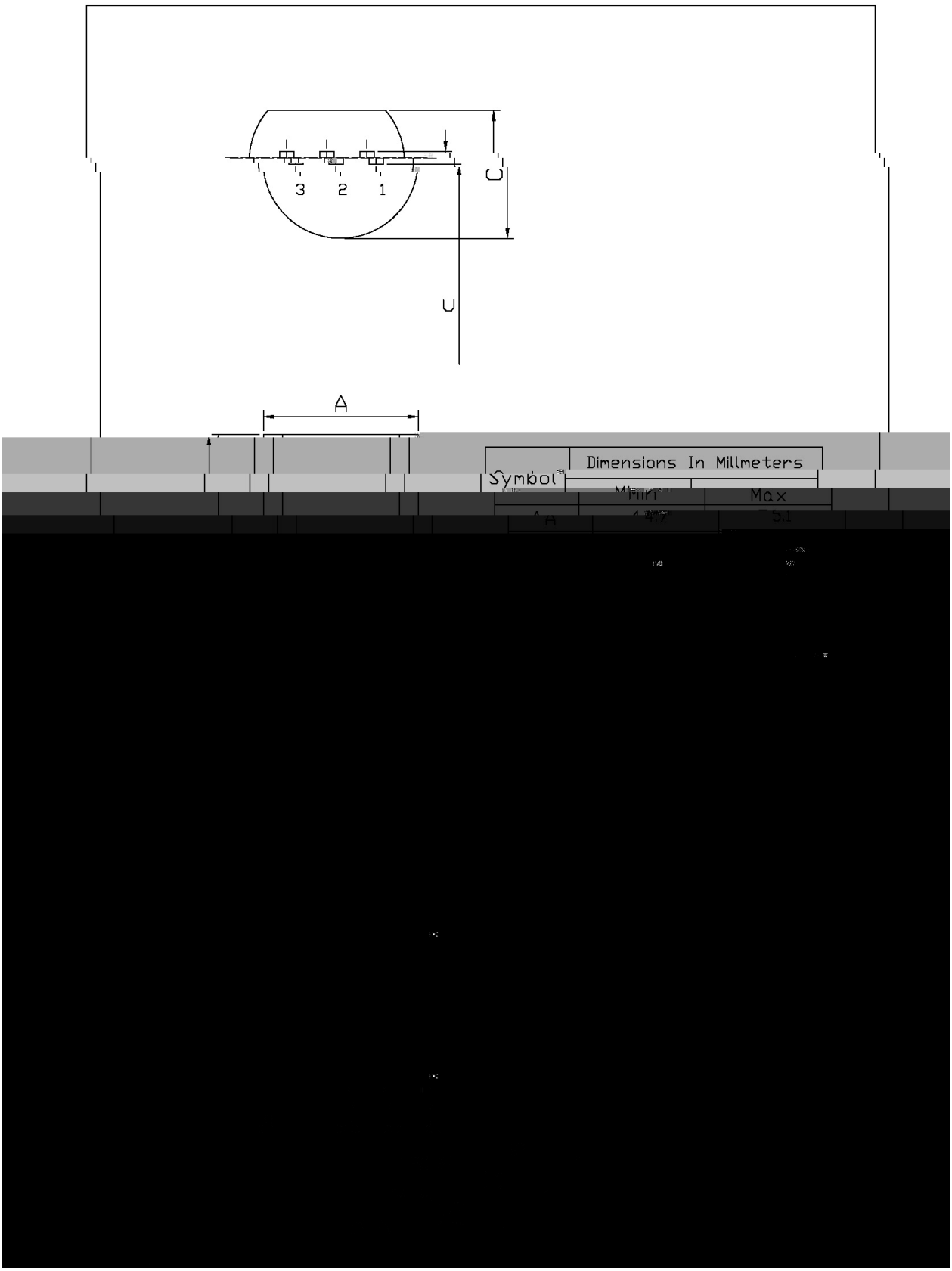


VCE(sat) – Ic

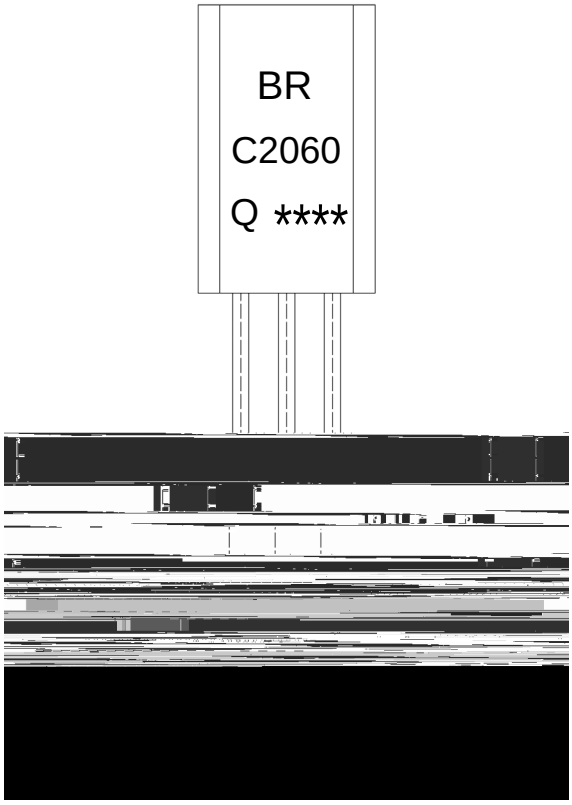


/ Package Dimensions

TO-92LM Unit: mm



/ Marking Instructions



Note:	
BR:	Company Code.
C2060:	Product Type.
Q:	h_{FE} Classifications Symbol
****:	Lot No. Code,code change with Lot No.

( ) / Temperature Profile for Dip Soldering(Pb-Free)



ب ˝ χ

1 Ĺ ♬ □ ỷ 25 Ḱ 150 ł ı `H ڑ 60 Ḱ 90sec;

2 Š Ö □ ỷ 255 ° 5 ł ı `H ڑ `Y ﾊ ㎛ 5 ° 0.5sec;

3 ì ˙ ﾘ ˛ ɔ ﻉ ỷ ㎛ 2 Ḱ 10 ł /sec.

Note:

1.Preheating:25~150 ł , Time:60~90sec.

2.Peak Temp.:255 ° 5 ł , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ł /sec.

/ Resistance to Soldering Heat Test Conditions

□ ỷ χ 270 ° 5 ł `H ڑ χ 10 ° 1 sec. Temp.:270±5 Time:10±1 sec

/ Packaging SPEC.

¨Ω ţ ﾘ Ł / BULK

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm3)		
	Units/Bag 只/袋	Bags/Inner Box 袋/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Bag 袋	Inner Box 盒	Outer Box 箱
TO-92LM	1,000	8	8,000	5	40,000	135×190	237×172×102	560×245×195

ﾊ Ⅴ ﾘ Ł / AMMO

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm3)	
	Units/tape 只/纸带	Tape/Inner Box 纸带/盒	Rows/Inner Box 纸带层/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Inner Box 盒	Outer Box 箱
TO-92LM	2,500	1	100	10	25,000	328×230×42	小箱 480×346×235 大箱 547×407×268

/ Notices