/ Descriptions

WR0<5 ṂṂ Ḅ SQS ÆṀ ч s ‖ ㄱ Silicon PNP transistor in a TO-92 Plastic Package.

/ Features

Y_{HER} ㅊ ı ㅀ 5VG ; ;< Ӈ ´

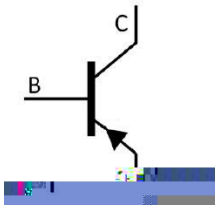
High V_{EBO}, complementary pair with 2SD889.

/ Applications

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Audio frequency amplifier.

/ Equivalent Circuit



/ Pinning



PIN1 χ Base PIN 2 χ Collector PIN 3 χ Emitter

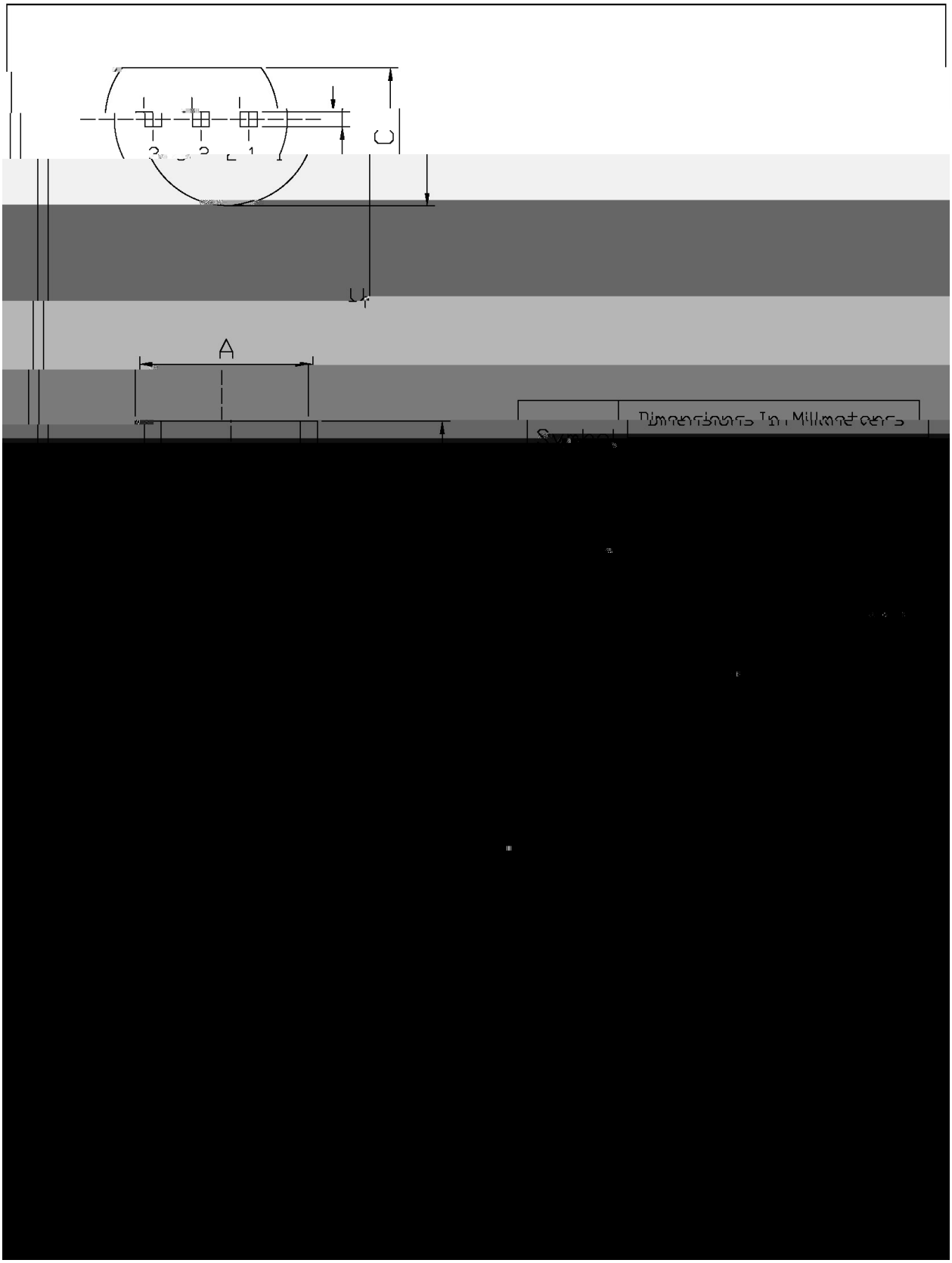
/ hFE Classifications & Marking

h_{FE} Classifications Symbol	P	Q	R	S	T
h_{FE} Range	120~200	160~260	210~340	290~460	400~650

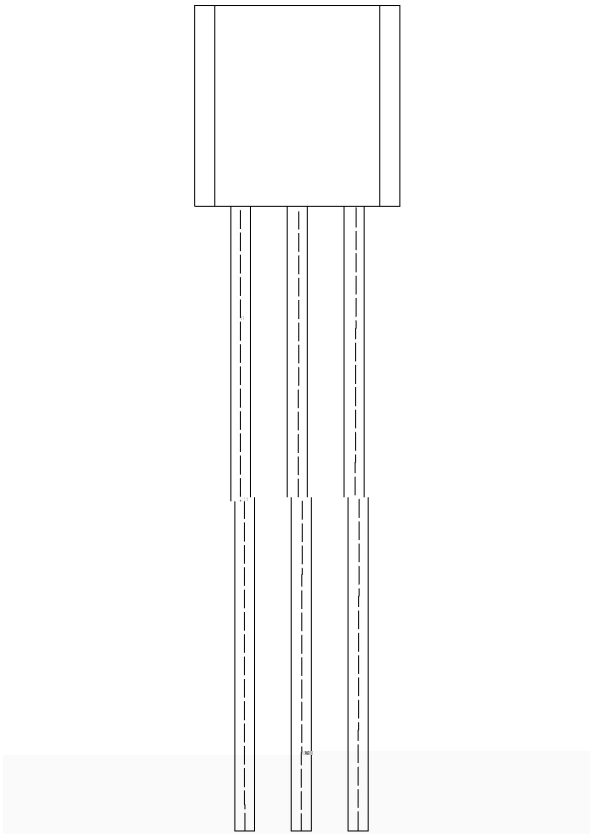
v ̈Ω	ϒ N	̈Ω Θ
Parameter	Symbol	

/ Package Dimensions

TO-92 Unit: mm

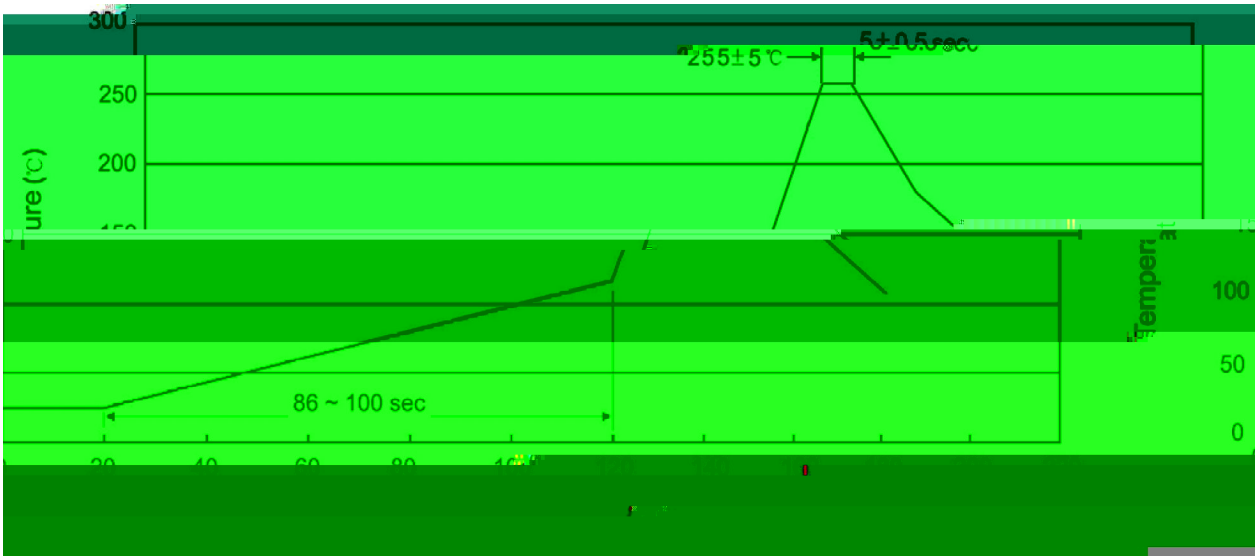


/ Marking Instructions



ب ˝ X

( ) / Temperature Profile for Dip Soldering(Pb-Free)



ب ˝ χ

1 Ĺ ♬ □ ỷ 25 Ḱ 150 ɬ ı ˋH ڑ 60 Ḱ 90sec;

2 Š Ө □ ỷ 255 ° 5 ɬ ı ˋH ڑ ˋY ﾊ ㎛ 5 ° 0.5sec;

3 ì ˙ ﾘ ˛ ɔ ෯ ỷ ㎛ 2 Ḱ 10 ɬ /sec.

Note:

1.Preheating:25~150 ɬ , Time:60~90sec.

2.Peak Temp.:255 ° 5 ɬ , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ɬ /sec.

/ Resistance to Soldering Heat Test Conditions

□ ỷ χ 270 ° 5 ɬ	ˋH ڑ χ 10 ° 1 sec.	Temp:270±5	Time:10±1 sec

/ Packaging SPEC.

ˇQ ţ ﾘ Ł / BULK

Package Type	Units	Dimension	(unit mm3)