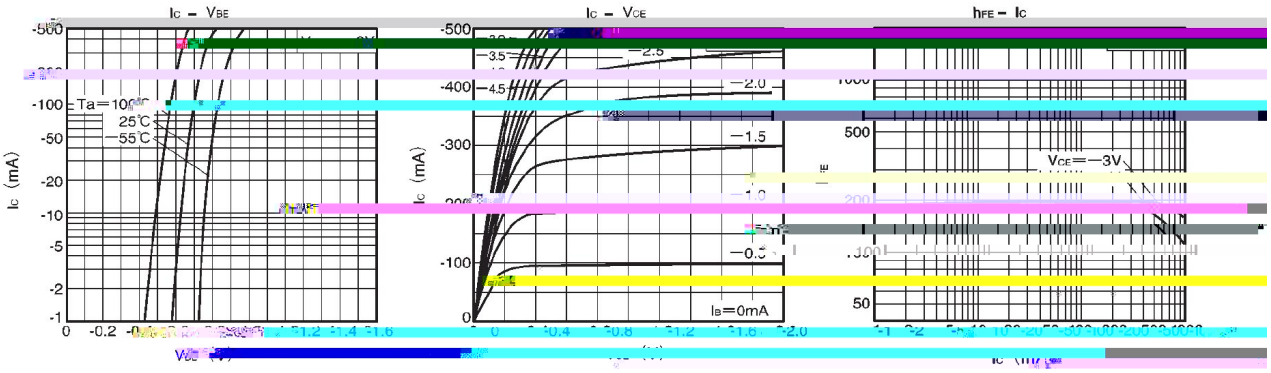
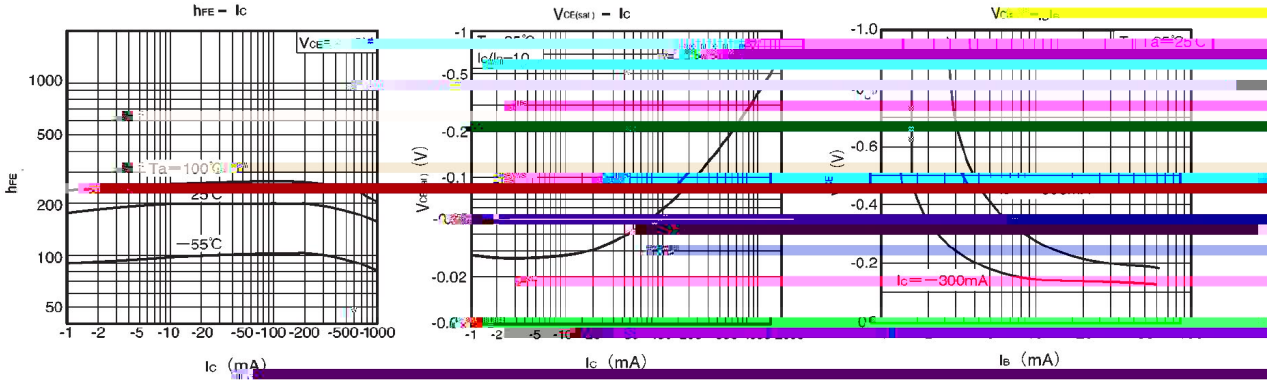
WR0<50P ḾḾÞ SQS ÆḾч s ll ㄱ Silicon PNP transistor in a TO-92LM Plastic Package.

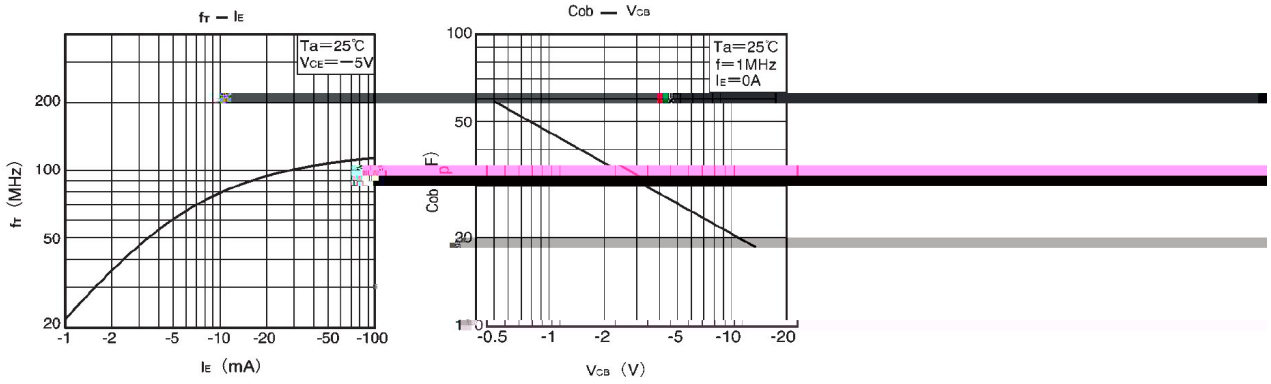
ت ᄀ я ت Ӈ/лъ 5VG4;8; ЙӴ
Low saturation voltage, complements the 2SD1858.

Ɇ̂ ᴀ ı ' Ѽ ᶻ
tTT2 -7 4D0-7 17(.00018 Tc.224 (Silicoymbol(w)9ans/TT2 45 71TD-.574(kD001 Tc4.002)9a254(k)TjP Q Ma8)-6Rs

/ Electrical Characteristic Curve

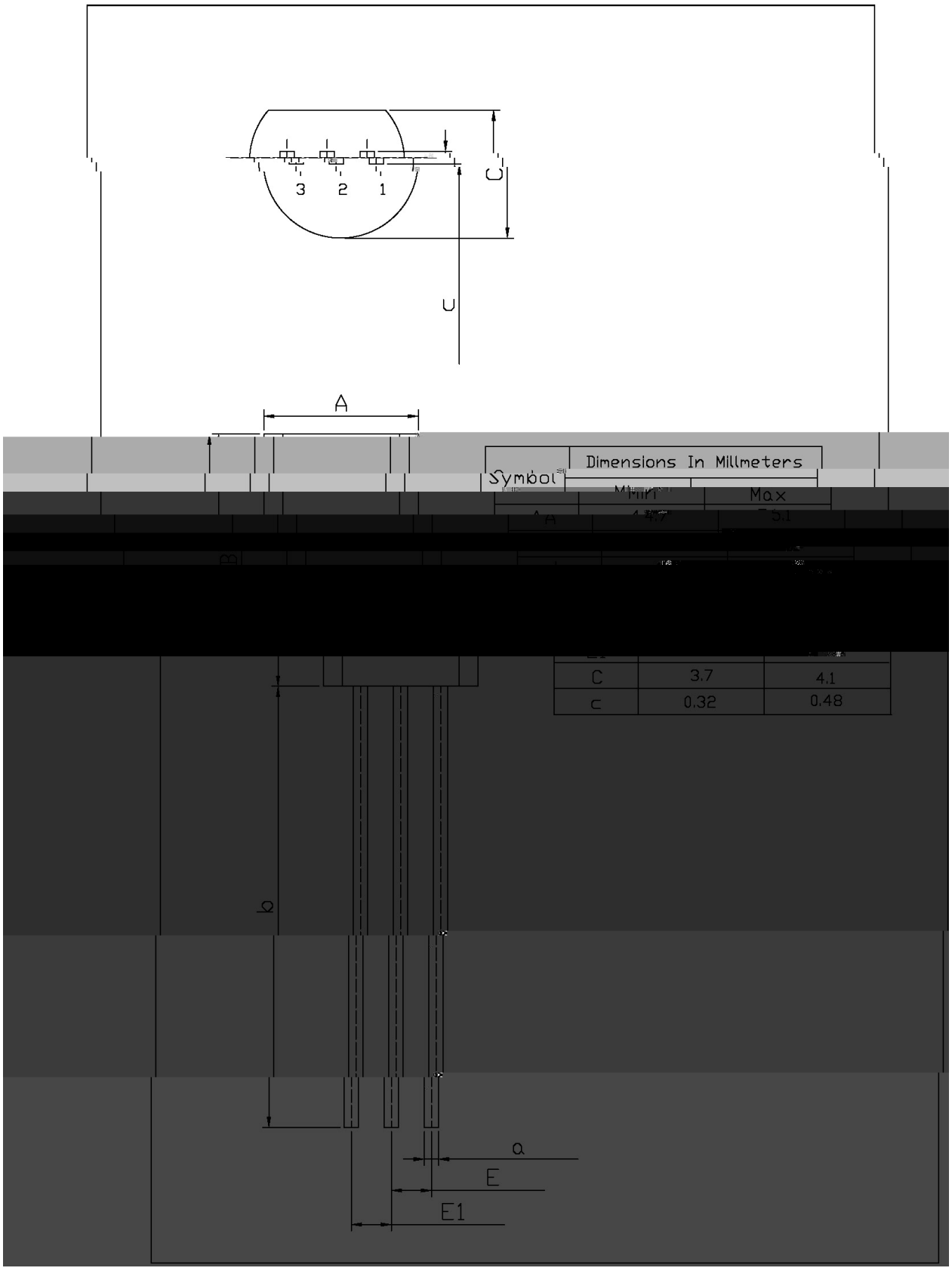




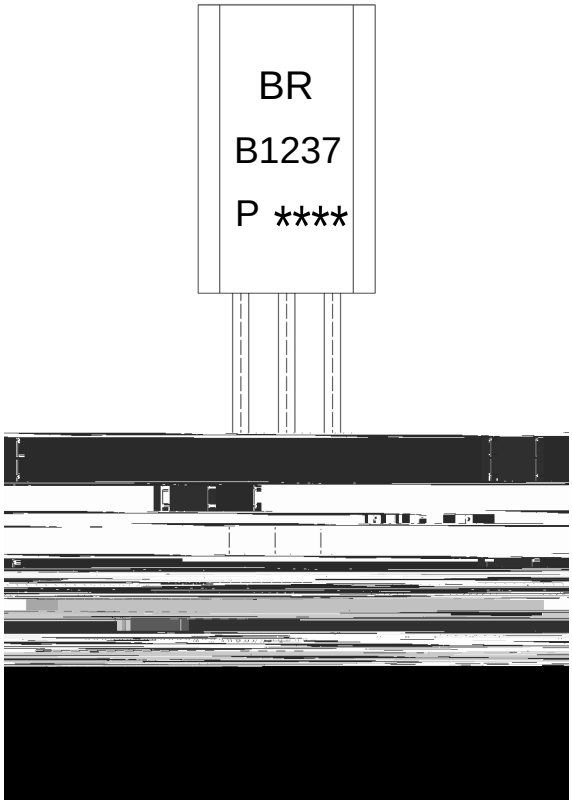


/ Package Dimensions

TO-92LM Unit: mm



/ Marking Instructions



ب ″ χ	
EU=	₩ ३ И Ґђ
E456 : χ	₩ ȝ ᴺ Ґђ
S=	₩ k ᵢₕ ʿ ⁶ Ґђ
---- χ	₩ ł ғ ń ᴺ Ґђ ı ₰ ł ғ ń ᴺ Ψ ק

Note:

BR:	Company Code.
B1237:	Product Type.
P:	h_{FE} Classifications Symbol
****:	Lot No. Code,code change with Lot No.

( ) / Temperature Profile for Dip Soldering(Pb-Free)



ب ˝ χ

1 Ĺ ♬ □ ỷ 25 Ḱ 150 ɬ ı `H ڑ 60 Ḱ 90sec;

2 Š Ө □ ỷ 255 ° 5 ɬ ı `H ڑ `Y ﾊ ᠷ 5 ° 0.5sec;

3 ì ˙ ϒ ˛ ɔ ᡐ ỷ ᠷ 2 Ḱ 10 ɬ /sec.

Note:

1.Preheating:25~150 ɬ , Time:60~90sec.

2.Peak Temp.:255 ° 5 ɬ , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ɬ /sec.

/ Resistance to Soldering Heat Test Conditions

□ ỷ χ 270 ° 5 ɬ `H ڑ χ 10 ° 1 sec. Temp.:270±5 Time:10±1 sec

/ Packaging SPEC.

¨Ǫ ţ ϒ Ь / BULK

Package Type	Units					Dimension		(unit mm3)
	Units/Bag /	Bags/Inner Box /	Units/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Bag	Inner Box	Outer Box
TO-92LM	1,000	8	8,000	5	40,000	135×190	237×172×102	560×245×195

ﾊ Ѵ ϒ Ь / AMMO

Package Type	Units					Dimension	(unit mm3)
	Units/tape /	Tape/Inner Box /	Rows/Inner Box /	Inner Boxes/Outer Box /	Units/Outer Box /	Inner Box	Outer Box
TO-92LM	2,500	1	100	10	25,000	328×230×42	480×346×235 547×407×268

/ Notices