2SA673(A)

Rev.E Mar.-2016

DATA SHEET

/ Descriptions

WR0<5 ṂṂ Ŀ SQS Æ Ṁ ɥ s ǁ ٦ Silicon PNP transistor in a TO-92 Plastic Package.

љ 5VF4546+D, Й ´

Complementary pair with 2SC1213(A).

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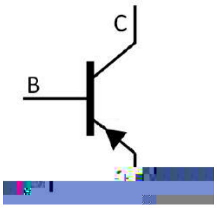
Low frequency power amplifier.



PIN1 χ Base PIN 2 χ Collector PIN 3 χ Emitter

/ hFE Classifications & Marking

/ Absolute Maximum Ratings(Ta=25)

v ῼ Parameter	ᅥ ᴺ Symbol		ῼӪ Rating	ᴅ ḫ Unit
Collector to Base Voltage	V_{CBO}	2SA673	-35	V
		2SA673A	-50	
Collector to Emitter Voltage	V_{CEO}	2SA673	-35	V
		2SA673A	-50	
Emitter to Base Voltage	V_{EBO}		-4.0	V
Collector Current - Continuous	I_C		-500	mA
Collector Power Dissipation	P_C		400	mW
Junction Temperature	T_j		150	
Storage Temperature Range	T_{stg}		-55 150	

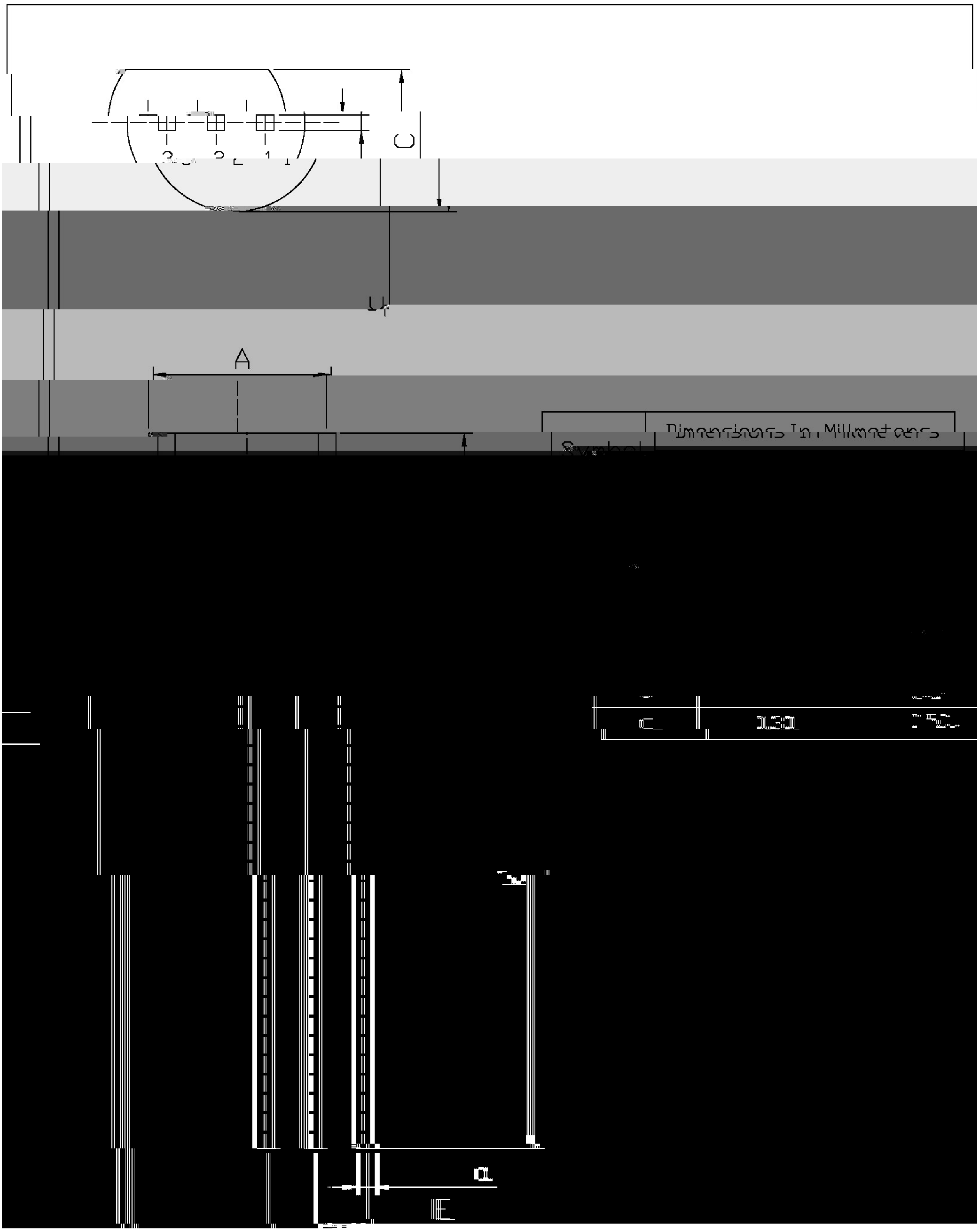
/ Electrical Characteristics(Ta=25)

v ῼ Parameter	ᅥ ᴺ Symbol		╜ ⁝ꜛ Ţ Test Conditions	ῳ ṇ Ӫ Min	Ɛ ʒ Ӫ Typ	ῳ ᶻ Ӫ Max	ᴅ ḫ Unit
Collector to Base Breakdown Voltage	V_{CBO}	2SA673	I_C=-10 A I_E=0	-35			V
		2SA673A		-50			
Collector to Emitter Breakdown Voltage	V_{CEO}	2SA673	I_C=-1.0mA I_B=0	-35			V
		2SA673A		-50			
Emitter to Base Breakdown Voltage	V_{EBO}		I_E=-10 A I_C=0	-4.0			V
Collector Cut-Off Current	I_{CBO}		V_{CB}=-20V I_E=0			-0.5	A
DC Current Gain	$h_{FE(1)}$		V_{CE}=-3.0V I_C=-10mA	60		320	
	$h_{FE(2)}$		V_{CE}=-3.0V I_C=-500mA	10			
Collector to Emitter Saturation Voltage	$V_{CE(sat)}$		I_C=-150mA I_B=-15mA		-0.2	-0.6	V
Base to Emitter Voltage	V_{BE}		V_{CE}=-3.0V I_C=-10mA		-0.64		V

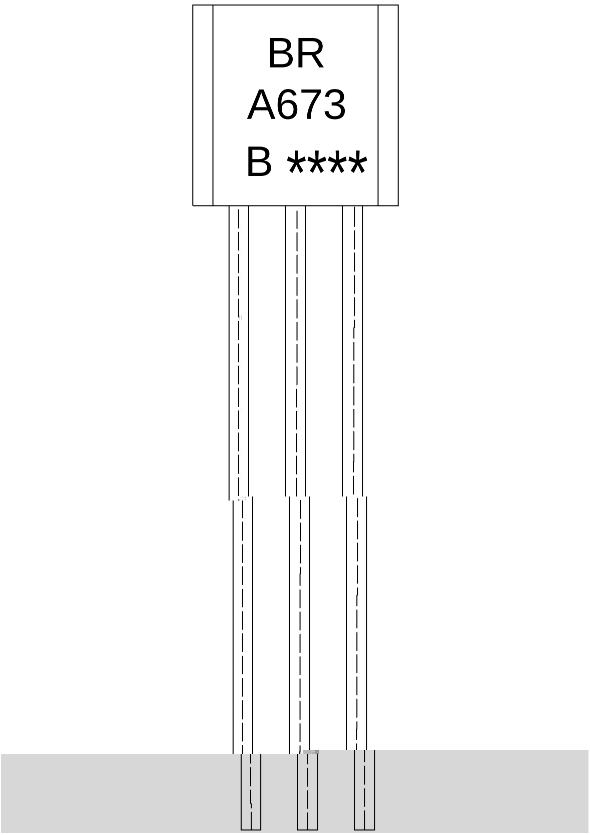
[redacted]

/ Package Dimensions

TO-92 Unit: mm



/ Marking Instructions



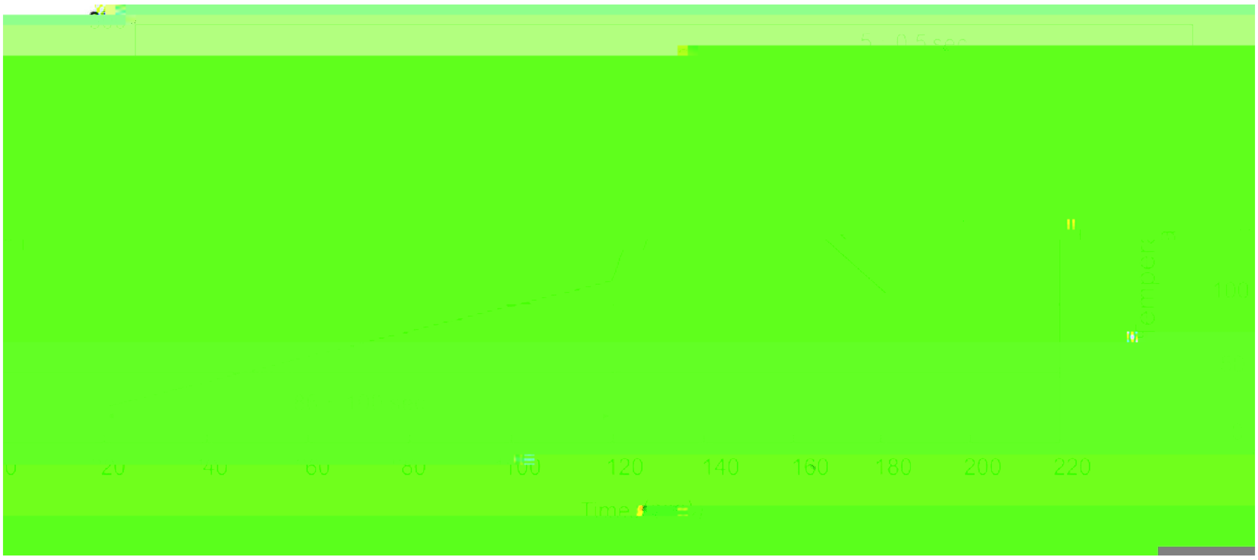
ب ˝ χ

EU꞊	ѫ ʅ ᴎ Ꮒ
D9 : 6 χ	ѫ ʒ ᴺ Ꮒ
E꞊	ѫ h_{FE} ˂ ⁶ Ꮒ
- - - - χ	ѫ ꞁ ғ ή ᴺ Ꮒ ı ɞ ꞁ ғ ή ᴺ ψ ק

Note:

BR:	Company Code.
A673:	Product Type.
B:	h_{FE} Classifications Symbol
****:	Lot No. Code,code change with Lot No.

( ) / Temperature Profile for Dip Soldering(Pb-Free)



ب ˝ χ
1 ͥ ♬ □ ŷ 25 Ḱ 150 ł ι `H ڑ 60 Ḱ 90sec;
2 Š Ӫ □ ŷ 255 ° 5 ł ι `H ڑ `Y ﾊ ㎳ 5 ° 0.5sec;
3 ì ˙ Ϳ ˛ ɔ ஃ ŷ ㎳ 2 Ḱ 10 ł /sec.

Note:
1.Preheating:25~150 ł , Time:60~90sec.
2.Peak Temp.:255 ° 5 ł , Duration:5 ° 0.5sec.
3. Cooling Speed: 2~10 ł /sec.

/ Resistance to Soldering Heat Test Conditions

□ ŷ χ 270 ° 5 ł `H ڑ χ 10 ° 1 sec.

Temp:270±5 Time:10±1 sec

/ Packaging SPEC.

ῼ ҭ ɣ Ŀ / BULK

Package Type 封装形式	Units 包装数量					Dimension 包装尺寸 (unit: mm3)		
	Units/Bag 只/袋	Bags/Inner Box 袋/盒	Units/Inner Box 只/盒	Inner Boxes/Outer Box 盒/箱	Units/Outer Box 只/箱	Bag 袋	Inner Box 盒	Outer Box 箱