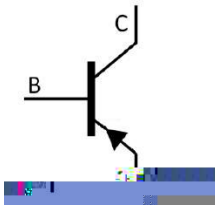
/ Descriptions

/ Features

/ Applications

/ Equivalent Circuit



/ Pinning

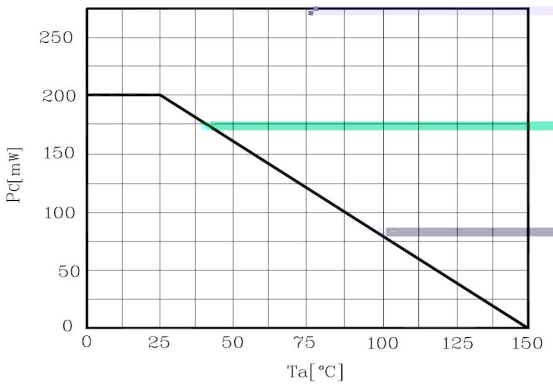


/ h_{FE} Classifications & Marking

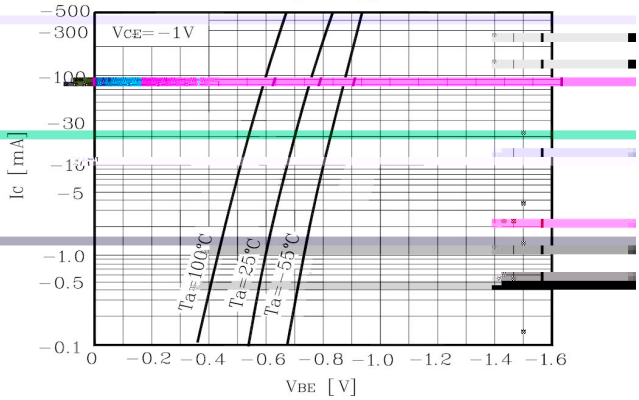
. -40(88]TJV0.8857.48 0.479.06 re f 536.46 8[(ht 0.48 30.06 603 6.46 8 1.25148 0.47998 30.603 6.46 T 10.1.25148 0.48001 30.60

/ Electrical Characteristic Curve

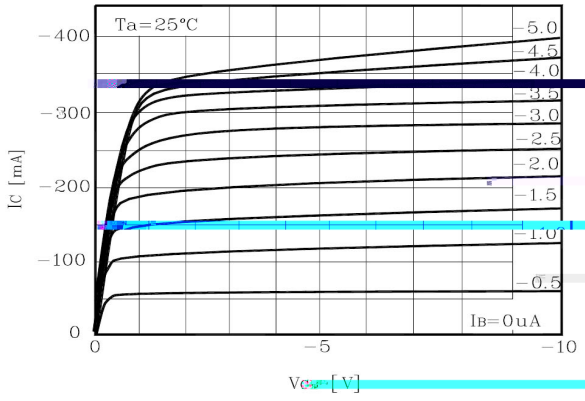
P_C - T_a



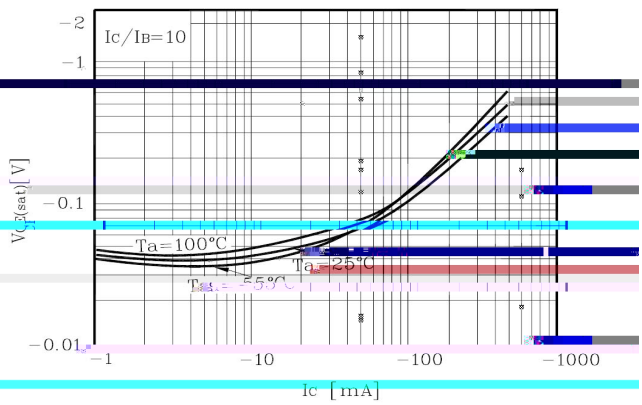
I_C - V_{BE}



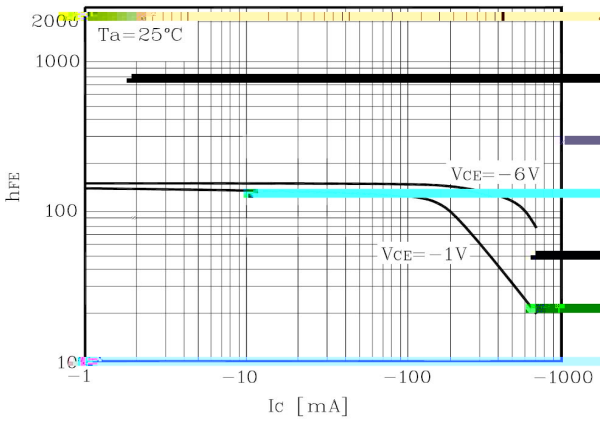
I_C - V_{CE}



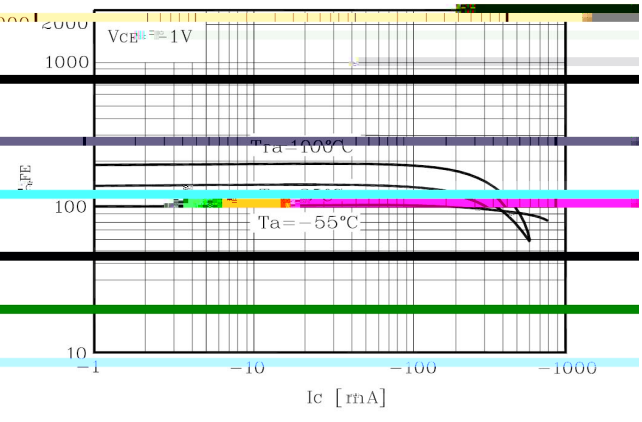
$V_{CE(sat)}$ - I_C



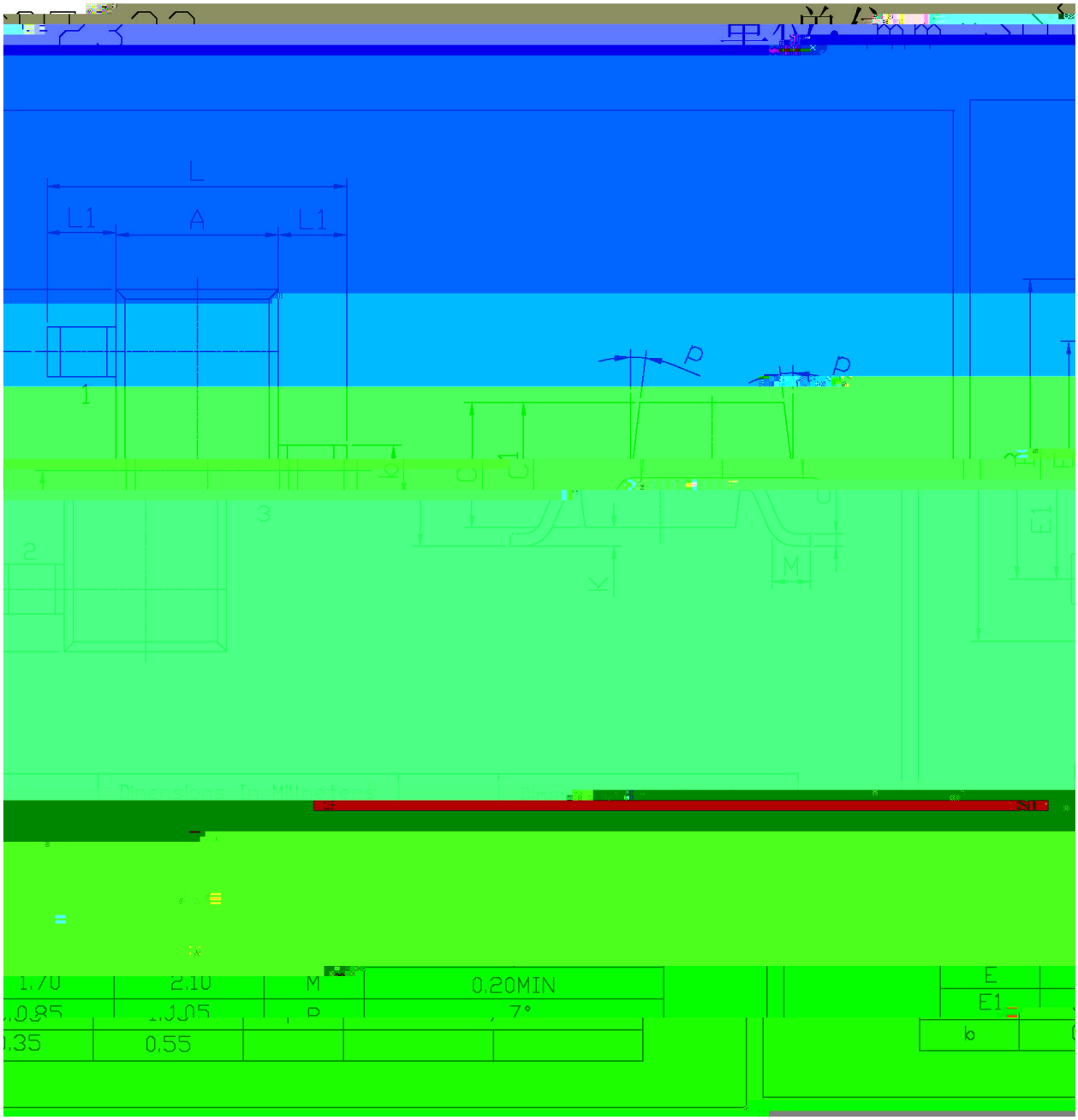
h_{FE} - I_C



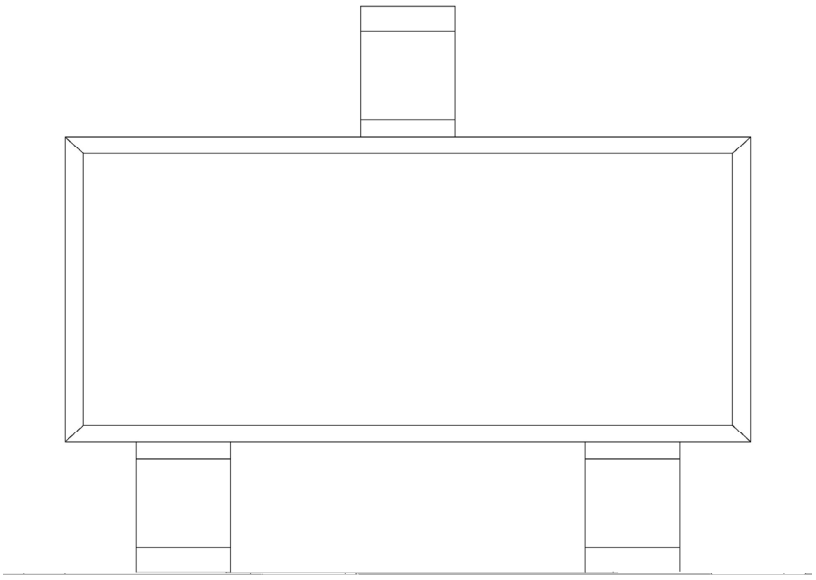
h_{FE} - I_C



/ Package Dimensions



/ Marking Instructions



( ) / Temperature Profile for IR Reflow Soldering(Pb-Free)

