2SA1316

Rev.E Mar.-2016

DATA SHEET

/ Descriptions

WR0<5 ĮMĮ Ł SQS ÆḾ ų s ‖ ˥ Silicon PNP transistor in a TO-92 Plastic Package.

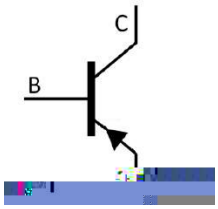
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ᵍ ‖ ề ɔ ʆ ņ / ƍ ᵠ ˬ Ѿ Ң/ ˡ љ 5VF665< Й ´

Low rbb´, low noise figure, complementary pair with 2SC3329

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Low noise audio amplifier applications.





PIN1 χ Base PIN 2 χ Collector PIN 3 χ Emitter

/ hFE Classifications & Marking

v ‴Ω Parameter	ϒ N Symbol	‴Ω Ö Rating	D Ч Unit
Collector to Base Voltage	V_{CBO}	-80	V
Collector to Emitter Voltage	V_{CEO}	-80	V
Emitter to Base Voltage	V_{EBO}	-5.0	V
Collector Current - Continuous	I_C	-100	mA
Base Current - Continuous	I_B	-20	mA
Collector Power Dissipation	P_C	400	mW

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( ) / Temperature Profile for Dip Soldering(Pb-Free)



ب ˝ χ

1 Ĺ ♬ □ ỷ 25 Ḱ 150 ł ı ˋH ڑ 60 Ḱ 90sec;

2 Š Ө □ ỷ 255 ° 5 ł ı ˋH ڑ ˋY ﾊ ㎛ 5 ° 0.5sec;

3 ì ˙ ﾘ ˛ ↄ ෯ ỷ ㎛ 2 Ḱ 10 ł /sec.

Note:

1.Preheating:25~150 ł , Time:60~90sec.

2.Peak Temp.:255 ° 5 ł , Duration:5 ° 0.5sec.

3. Cooling Speed: 2~10 ł /sec.

/ Resistance to Soldering Heat Test Conditions

□ ỷ χ 270 ° 5 ł	ˋH ڑ χ 10 ° 1 sec.	Temp:270±5	Time:10±1 sec

/ Packaging SPEC.

ˈQ ţ ﾔ Ł / BULK